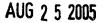
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Date:

August 25, 2005

To:

Kiesha L. Rosc - 2822

Location:

United States Patent and Trademark Office

Fax No.:

(571) 273-8300

From:

Robert L. King - 30,185

Subject:

10/705,317 - Leo Mathew et al.

Attny Docket No. SC12733TP

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MESSAGE:

Enclosed herewith, please find FORMAL AMENDMENT for filing in the below-identified application. If Applicant has overlooked any fees, or if any overpayment has been made, the Commissioner is hereby authorized to credit or debit Deposit 503079, Freescale Semiconductor, Inc.

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EXAMINER: Kiesha L. Rose GROUP ART UNIT: 2822 SERIAL NO.: 10/705,317 FILED: NOVEMBER 10, 2003

INVENTOR: LEO MATHEW ET AL.

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DOCKET NO. SC12733TP

UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT

Leo Mathew et al.

GROUP ART UNIT: 2822

APPLN, NO.:

10/705,317

EXAMINER:

Kiesha L. Rose

FILED:

November 10, 2003

CONFIRMATION No.: 4446

TITLE:

TRANSISTOR HAVING THREE ELECTRICALLY ISOLATED

ELECTRODES AND METHOD OF FORMATION

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AMENDMENT UNDER 37 CFR 1.116

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to a final rejection in an Office action mailed July 27, 2005, Applicants are filing an amendment after final and request entrance of the following amendment to place the application in condition for allowance.

FEES

No additional fees are owed in connection with this amendment. However, Applicants continue to authorize the charging of Deposit Account No. 503079 (Freescale Semiconductor, Inc.) for any fees owed in connection with this application, or credit Deposit Account No. 503079 (Freescale Semiconductor, Inc.) for any refunds: